IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| Application | Serial Number |
|-------------|--------------------------------------|
| Filing Date | |
| Inventor | |
| Assignee | Micron Technology, Inc. |
| Group Art | Number |
| Examiner | |
| Attorney's | Docket No |
| Title: | Methods of Forming a Transistor Gate |

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether all the submitted references are prior art.

Respectfully submitted,

ted: 8/11/00 Attorney:

James D. Shaurette

Reg. #39,833

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